45TH DAC BEST PAPER CANDIDATES

10.2 DeFer: Deferred Decision Making Enabled Fixed- outline Floorplanner
Jackey Z. Yan - *Iowa State Univ., Ames, IA*
Chris Chu - *Iowa State Univ., Ames, IA*

11.1 Optimality and Improvement of Dynamic Voltage Scaling Algorithms for Multimedia Applications
Zhen Cao - *Univ. of California, Los Angeles, CA*
Brian Foo - *Univ. of California, Los Angeles, CA*
Lei He - *Univ. of California, Los Angeles, CA*
Mihaela van der Schaar - *Univ. of California, Los Angeles, CA*

12.3 Speedpath Prediction Based on Learning From a Small Set of Examples
Pouria Bastani - *Univ. of California, Santa Barbara, CA*
Kip Killpack - *Intel Corp., Hillsboro, OR*
Li-C. Wang - *Univ. of California, Santa Barbara, CA*
Eli Chiprout - *Intel Corp., Hillsboro, OR*

19.1 Analysis of Variability-tolerance in Chip-multiprocessors
Sebastian X. Herbert - *Carnegie Mellon Univ., Pittsburgh, PA*
Diana Marculescu - *Carnegie Mellon Univ., Pittsburgh, PA*

23.2 Formal Datapath Representation and Manipulation for Implementing DSP Transforms
Peter A. Milder - *Carnegie Mellon Univ., Pittsburgh, PA*
Franz Franchetti - *Carnegie Mellon Univ., Pittsburgh, PA*
James C. Hoe - *Carnegie Mellon Univ., Pittsburgh, PA*
Markus Pueschel - *Carnegie Mellon Univ., Pittsburgh, PA*

27.4 Automated Design of Tunable Impedance Matching Networks for Reconfigurable Wireless Applications
Arthur Nieuwoudt - *Rice Univ., Houston, TX*
Jamil Kawa - *Synopsys, Inc., Mountain View, CA*
Yehia Massoud - *Rice Univ., Houston, TX*

28.2 Predictive Formulae for OPC with Applications to Lithography-friendly Routing
Tai-Chen Chen - *National Central Univ., Taoyuan, Taiwan and National Taiwan Univ., Taipei, Taiwan*
Guang-Wan Liao - *National Taiwan Univ., Taipei, Taiwan*
Yao-Wen Chang - *National Taiwan Univ., Taipei, Taiwan*

35.1 Variation-adaptive Feedback Control for Networks-on-Chip with Multiple Clock Domains
Umit Y. Ogras - *Carnegie Mellon Univ., Pittsburgh, PA*
Radu Marculescu - *Carnegie Mellon Univ., Pittsburgh, PA*
Diana Marculescu - *Carnegie Mellon Univ., Pittsburgh , PA*

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14.1 WavePipe: Parallel Transient Simulation of Analog and Digital Circuits on Multi-core Sharedmemory Machines
Wei Dong - *Texas A&M Univ., College Station, TX*
Peng Li - *Texas A&M Univ., College Station, TX*
Xiaoji Ye - *Texas A&M Univ., College Station, TX*

22.4 IFRA: Instruction Footprint Recording and Analysis for Post-silicon Bug Localization inProcessors
Sung-Boem Park - *Stanford Univ., Palo Alto, CA*
Subhasish Mitra - *Stanford Univ., Palo Alto, CA*