

# Awards

## 44th DAC Best Paper Candidates

Fifteen papers were nominated by the Technical Program Committee as a DAC Best Paper Candidate; six in front-end design and nine in back-end design. Final decisions will be made after the papers are presented at the conference. The awards for the best papers, one in front-end design and one in back-end design, will be presented at 12:30 on Thursday, June 7 in Ballroom 20ABC, just before the Keynote Address.

- 3.4 *Simulink-Based MPSoC Design Flow: Case Study of Motion-JPEG and H.264*
- 6.2 *Width-dependent Statistical Leakage Modeling for Random Dopant Induced Threshold Voltage Shift*
- 8.1 *Voltage-Frequency Island Partitioning for GALS-based Networks-on-Chip*
- 9.1 *Interdependent Latch Setup/Hold Time Characterization via Euler-Newton Curve Tracing on State-Transition Equations*
- 13.1 *Endurance Enhancement of Flash-Memory Storage Systems: An Efficient Static Wear Leveling Design*
- 14.1 *Comparative Analysis of Conventional and Statistical Design Techniques*
- 16.1 *Period Optimization for Hard Real-time Distributed Automotive Systems*
- 17.3 *Towards An Ultra-Low-Power Architecture Using Single-Electron Tunneling Transistors*
- 20.1 *Characterization and Estimation of Circuit Reliability Degradation under NBTI using On-Line IDDQ Measurement*
- 23.1 *Progressive Decomposition: A Heuristic to Structure Arithmetic Circuits*
- 24.1 *Parameter Finding Methods for Oscillators with a Specified Oscillation Frequency*
- 27.2 *RQL: Global Placement via Relaxed Quadratic Spreading and Linearization*
- 30.4 *New Test Data Decompressor for Low Power Applications*
- 33.3 *On-The-Fly Resolve Trace Minimization*
- 34.1 *An Integer Linear Programming Based Routing Algorithm for Flip-Chip Design*

## Congratulations Best Paper Winners!

### 16.1 *Period Optimization for Hard Real-time Distributed Automotive Systems*

**Abhijit Davare**, Qi Zhu - *Univ. of California, Berkeley, CA*  
**Marco Di Natale** - *General Motors Corp., Warren, MI*  
**Claudio Pinello** - *Cadence Design Systems, Inc., Berkeley, CA*  
**Sri Kanajan** - *General Motors Corp., Warren, MI*  
**Alberto Sangiovanni-Vincentelli** - *Univ. of California, Berkeley, CA*

### 9.1 *Interdependent Latch Setup/Hold Time Characterization via Euler-Newton Curve Tracing on State-Transition Equations.*

**Shweta Srivastava**, Jaijeet Roychowdhury - *Univ. of Minnesota, Minneapolis, MN*